Efficient Evaluation of Lazy Programs, or Compilation of Call-by-Need

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This presentation is based on Simon P. Jones and David R. Lester's Implementing Functional Languages: a Tutorial

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This presentation is based on Simon P. Jones and David R. Lester's Implementing Functional Languages: a Tutorial

For a "modernized" version of its appendix, one can see https://github.com/Ailrun/core-lang-haskell

Advantages of Lazy Programs

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> Make it easier to deal with recursion when using combinator libraries

Make it easier to deal with recursion when using combinator libraries
 Bring us efficient persistent data structures

- Make it easier to deal with recursion when using combinator libraries
- Bring us efficient persistent data structures
- Provide a way to encode co-data (e.g. streams)

The Question

Are these advantages real?

Is it possible to efficiently execute a lazy program?

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1 Sharing

Sharing
 (Redirection)

Sharing
 (Redirection)
 Instantiation

Sharing
 (Redirection)
 Instantiation

What are These Difficulties?

Sharing
 (Redirection)
 Instantiation

- What are These Difficulties?
- How to Solve These Difficulties?



square x = x * x



How can we reduce this tree into 81?



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square x = x * x
main = square (square 3)

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We reduces a (directed) graph, not a tree.

Moreover, we need to *update* a node (so that multiple refereces share the evaluation cost)

Each graph reduction step should be as *local and small* as possible (for further optimizations, machine-compilability, etc.)

id x = x
square x = (id x) * x
main = square (square 3)



id x = x
square x = (id x) * x
main = square (square 3)



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How should we reduce the application node for id?





We lose sharing!





We need to modify an ancestor (depending on the depth of id)


How should we handle this?



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We follow this *redirection* node (#) when we reduce the parent

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We introduce an "run-time only" node (#) to handle it

Each graph reduction step should be as local and small as possible

Unfortunate "Change-all" Step



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Unfortunate "Change-all" Step



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Unfortunate "Change-all" Step



square x = x * x
main = square (square 3)

This changes almost entire structure of graph!

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When we instantiate a function definition as a part of a graph, we need to analyze the current graph **and** to construct a new graph

How can we divide this huge step into smaller steps?

Let's start with construction of graph of main itself

We construct it in a argument-first way, so start at 3

square x = x * x
main = square (square 3)

3

and then square

square x = x * x
main = square (square 3)

square 3

Now we can form an application node

square x = x * x
main = square (square 3)

3 square

Once we add another square



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We can finish the main graph by constructing an application node





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main = square (square 3)



To construct its application node, we need to construct the function node





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Now, clean up the old root



After visual rearrange, it is clear that we get the expected graph





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Can we translate this into recordable code pieces? Then we can "compile" main and square into those.

Let's repeat the main construction first.

We construct it in a bottom-up way, so start at 3

PushInt 3

and then square

PushInt 3 PushGlobal square

square x = x * x
main = square (square 3)

Now we can form an application node

square x = x * x
main = square (square 3)

PushInt 3 PushGlobal square MakeApp

Once we add another square

square x = x * x
main = square (square 3)

PushInt 3 PushGlobal square MakeApp PushGlobal square

We can finish the main graph by constructing an application node

square x = x * x
main = square (square 3)

PushInt 3 PushGlobal square MakeApp PushGlobal square MakeApp

One more step here: we need to continue the graph reduction process

square x = x * x
main = square (square 3)

PushInt 3 PushGlobal square MakeApp PushGlobal square MakeApp Unwind

Now let's compile square too

We construct the arugment first

Push 0

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To construct its application node, we need to construct the function node

Push 0 Push 1
Push 0 Push 1 PushGlobal *

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Push 0 Push 1 PushGlobal * MakeApp

and then the top-level application node

Push 0 Push 1 PushGlobal * MakeApp MakeApp

square x = x * x
main = square (square 3)

Now, clean up the old root

Push 0 Push 1 PushGlobal * MakeApp MakeApp Update 2

Pop 2

Note that we Update the root here to share the work

Push 0
Push 1
PushGlobal *
MakeApp
MakeApp
Update 2
Pop 2

Again, for the further evaluation, we need to add the Unwind instruction

Push 0 Push 1 PushGlobal * MakeApp MakeApp Update 2 Pop 2 Unwind

What does Unwind at the end of square do?



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It first checks whether the root is a global/primitive value



Otherwise, it steps into the function side



Until it reaches a global/primitive value



And jump to the code for the global



Unwind has linear time complexity to the length of a "spine" (the curried applications on a function)

To avoid the problem of a spine, we will use "closures" to represent a call

To avoid the problem of a spine, we will use "closures" to represent a call



To avoid the problem of a spine, we will use "closures" to represent a call



square x = x * x

But this

How can we build this?

square x = x * x



First, we take an argument

square x = x * x



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Then, we put it into stack twice for the * call



square x = x * x

Then, we put it into stack twice for the * call

square x = x * x



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Now we invoke *



square x = x * x

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We can translate this into a code form

square x = x * x



First, we take an argument

Take 1

square x = x * x

Then, we put it into stack twice for the * call

square x = x * x

Take 1 Push (Arg 0)

Then, we put it into stack twice for the * call

square x = x * x

Take 1 Push (Arg 0) Push (Arg 0)

Now we invoke *

square x = x * x

Take 1 Push (Arg 0) Push (Arg 0) Enter (Label *)

Note that we do not have anything corresponds to Update here.

In fact, only with these 3 instructions, we lose sharing of evaluations!

In fact, Update is one of the key issue of TIM. It is quite costly to have a correct sharing with TIM due to its machine-level representation details

Can we combine G-machine's simple memory structure (only pointers) and TIM's "closure"-based approach?

Let there be GHC

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G. L. Burn, S. L. P. Jones, and J. D. Robson. "The spineless G-machine". LFP'88

G. L. Burn, S. L. P. Jones, and J. D. Robson. "The spineless G-machine". LFP'88

 S. L. P. Jones and J. Salkild. "The spineless tagless G-machine". FPCA'89
 S. L. P. Jones. "Implementing lazy functional languages on stock hardware: the Spineless Tagless G-machine". JFP'92

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- ► S. L. P. Jones and J. Salkild. "The spineless tagless G-machine". FPCA'89
- S. L. P. Jones. "Implementing lazy functional languages on stock hardware: the Spineless Tagless G-machine". JFP'92
- L. Maurer, P. Downen, Z. M. Ariola, and S. L. P. Jones. "Compiling without continuations". PLDI'17

- In lazy evaluation, graph reduction is the key to handle sharing
- ► G-machine solves instantiation inefficiency but is still inefficient in its Unwind
- TIM removes some inefficiency of G-machine, but it also adds some for Update
- Their combinations can be more efficient... For that, see spineless tagless G-machine and "compiling without continuation"