

Memoization-based proof search in LF: an experimental evaluation of a prototype

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Outline

- LF as a logic programming framework
- Example: Type-system with subtyping
- Basics of tabled higher-order logic programming
- Experimental evaluation:
 1. Refinement type-checking:
Depth-first vs tabled search
 2. Parsing into higher-order abstract syntax:
Iterative deepening vs tabled search
- Related Work
- Conclusion and future work

LF as a logic programming framework

Logical framework LF [Harper,Honsell,Plotkin93]
dependently typed λ -calculus

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Framework for specifying and implementing

- logical systems
- proofs about them

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- proofs about them (correctness, soundness ...)

Proof search via higher-order logic programming
[Pfenning91]

- Terms: (dependently) typed λ -calculus
- Clauses: implication, universal quantification

Proof search over declarative systems

Proof search problems:

- Infinite computation leads to non-termination.
⇒ many specifications are not executable
- Redundant computation hampers performance.

“...it is very common for the proofs to have repeated sub-proofs that should be hoisted out and proved only once as lemmas.” [Necula, Lee97]

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“...it is very common for the proofs to have repeated sub-proofs that should be hoisted out and proved only once as lemmas.” [Necula, Lee97]

Solution: Memoization and re-use of sub-proofs

Memoization-based proof search

First-order tabelling [Tamaki,Sato86]

- Memoize atomic subgoals and re-use results
- Finds all possible answers to a query
- Terminates for programs in a finite domain
- Combine tabled and non-tabled execution
- Very successful: XSB system [Warren *et.al.*]

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Higher-order tabelling (see also [Pientka, ICLP'02])

- Proof-theoretic characterization
- This talk: **Experiments with higher-order tabling**

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Declarative description of subtyping

types $\tau ::= \text{zero} \mid \text{pos} \mid \text{nat} \mid \text{bit} \mid \tau_1 \Rightarrow \tau_2 \mid \dots$

Example: $6 = 110$ and $110 \in \text{nat}$

Declarative description of subtyping

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Example: $6 = 110$ and $110 \in \text{nat}$

$\frac{}{\text{zero} \preceq \text{nat}}$ zn

$\frac{}{\text{pos} \preceq \text{nat}}$ pn

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$\frac{}{T \preceq T}$ refl

$\frac{T \preceq R \quad R \preceq S}{T \preceq S}$ tr

Typing rules for Mini-ML

expressions $e ::= \epsilon \mid e\ 0 \mid e\ 1 \mid \text{lam } x.e \mid \text{app } e_1\ e_2$

$$\frac{\Gamma \vdash e : \tau' \quad \tau' \preceq \tau}{\Gamma \vdash e : \tau} \text{tp-sub}$$

$$\frac{\Gamma, x : \tau_1 \vdash e : \tau_2}{\Gamma \vdash \text{lam } x.e : \tau_1 \Rightarrow \tau_2} \text{tp-lam}^x$$

Implementation of subtyping

zn: sub zero nat.

pn: sub pos nat.

nb: sub nat bit.

refl: sub T T.

tr: sub T S

<- sub T R

<- sub R S.

Implementation of subtyping

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```
  <- sub T R
```

```
  <- sub R S.
```

Not executable!

Implementation of typing rules

```
tp_sub:   of E T
         <- of E T'
         <- sub T' T.
```

```
tp_lam:   of (lam  $\lambda x.E x$ ) (T1 => T2)
         <- ( $\Pi x:exp.$  of x T1 -> of (E x) T2).
```

“forall $x:exp$, assume of x T1
and show of (E x) T2”

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           “forall  $x:\text{exp}$ , assume of x T1
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Redundancy: `tp_sub` is always applicable!

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Tabled higher-order logic programming

- Eliminate redundant and infinite paths from proof search using a memo-table
- Table entry: $(\Gamma \rightarrow a, \mathcal{A})$
 - Γ : context of assumptions (i.e. $x:\text{exp}, u:\text{of } x \text{ T1}$)
 - a : atomic goal (i.e. of $(\text{lam } \lambda x. x) \text{ T}$)
 - \mathcal{A} : list of answer substitutions for all free variables in Γ and a
- Depth-first multi-stage strategy adopted from [Tamaki,Sato89]

How higher-order tabling works...

Stage 1

- \rightarrow of $(\text{lam } \lambda x.x) \text{ T}$

Entry	Answers

How higher-order tabling works...

Stage 1

• \rightarrow of $(\text{lam } \lambda x.x) \top$

Entry	Answers
• \rightarrow of $(\text{lam } \lambda x.x) \top$	

How higher-order tabling works...

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• \rightarrow of (lam $\lambda x.x$) T

$\xrightarrow{\text{tp_sub}}$ • \rightarrow of (lam $\lambda x.x$) R,
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Entry	Answers
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tp_lam	\rightarrow	x:exp, u:of x T1 \rightarrow of x T2	

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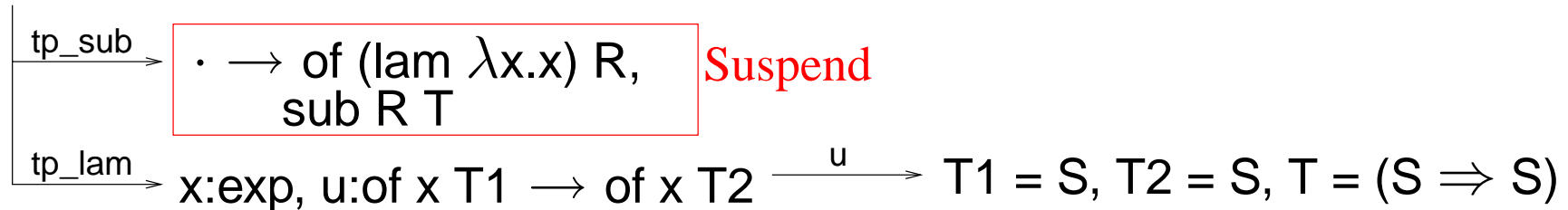
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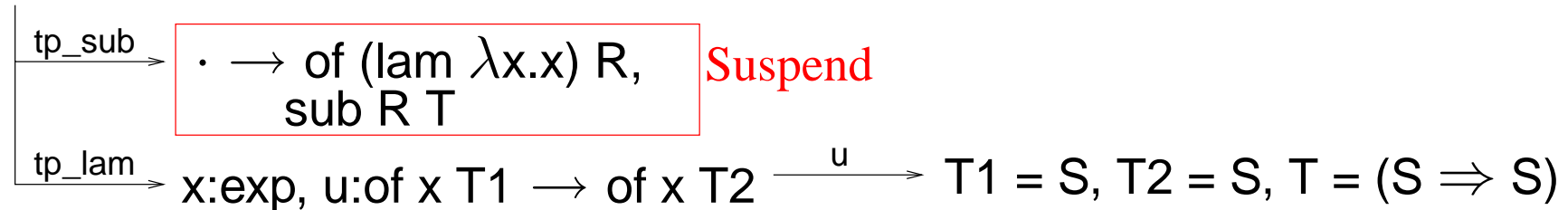


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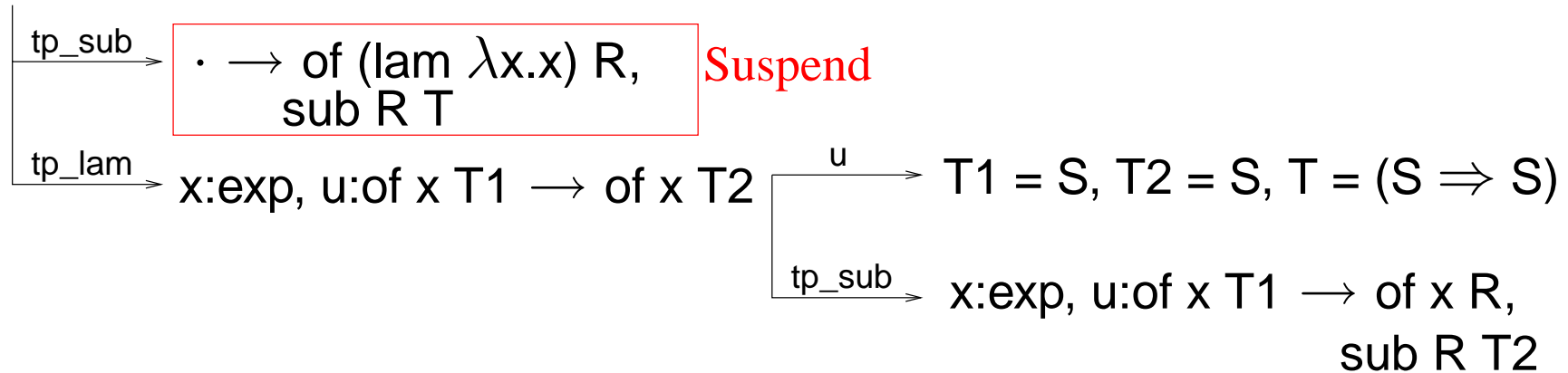


Entry	Answers
$\cdot \rightarrow$ of (lam $\lambda x.x$) T	$\text{T} = (\text{S} \Rightarrow \text{S})$
$x:\text{exp}, u:\text{of } x \text{ T1} \rightarrow \text{of } x \text{ T2}$	$\text{T1} = \text{S}, \text{T2} = \text{S}$

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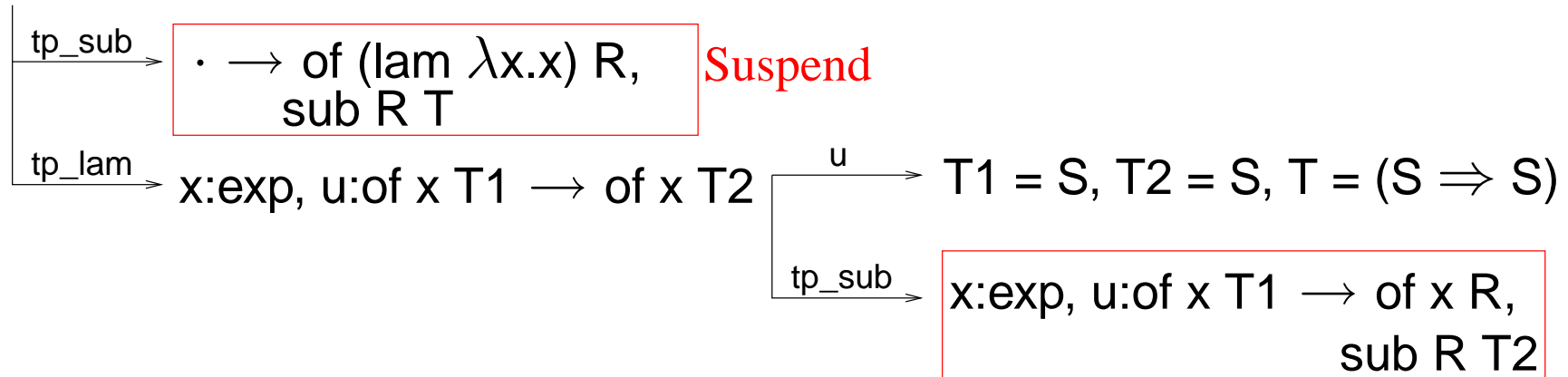


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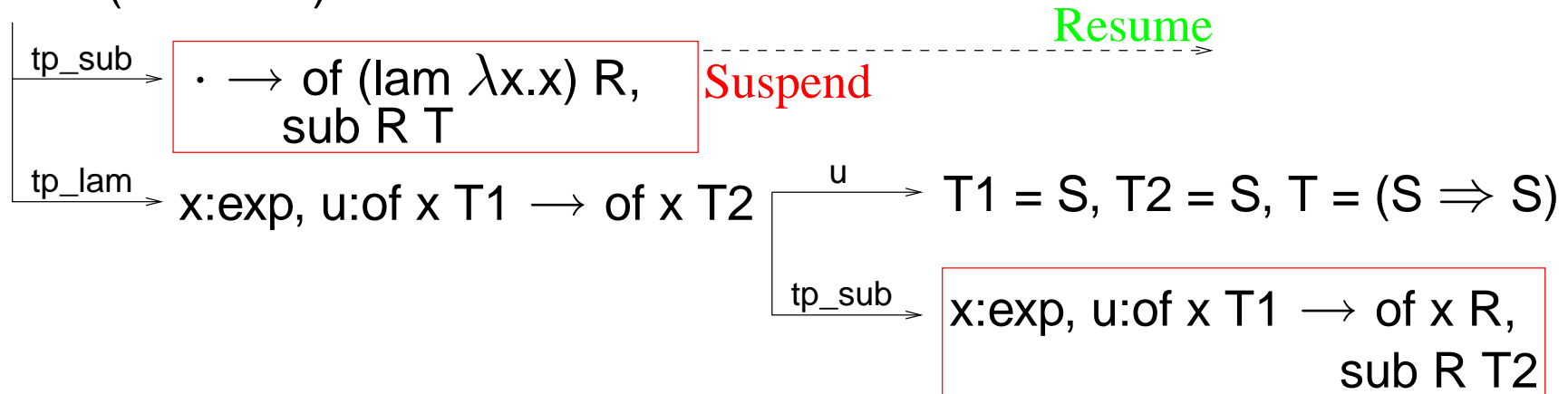
Suspend

Stage 1 finished

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Stage 1

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Entry	Answers
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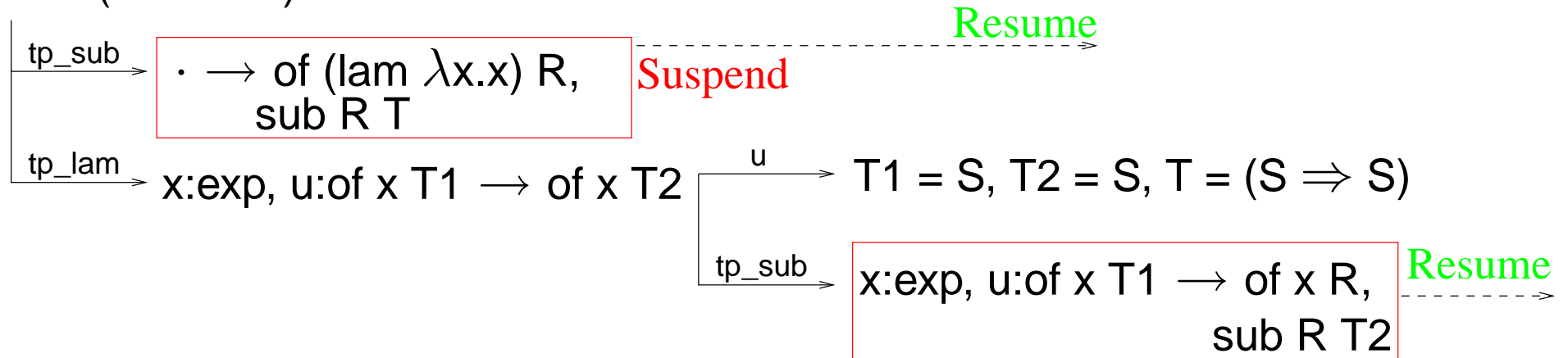
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Higher-order issues

- Dependencies among propositions

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- Subordination analysis [Virga99]

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Refinement type checking

- Type-inference with subtyping and intersections
- Bi-directional type-checking algorithm [Davies, Pfenning00]
- Distinguish between expressions for which
 1. a type can be **synthesized**
 2. can be **checked** against a given type

Depth-first vs Memoization(all solutions)

Program	Depth-First	Memoization
plus'4	483.070 sec	2.330 sec
plus4	696.730 sec	3.150 sec
plus4(np)	22.770 sec	1.95 sec
sub'1a	0.070 sec	0.240 sec
sub1b	3.88 sec	7.560 sec
sub3b	10.440 sec	11.200 sec
mult1(np)	1133.490 sec	4.690 sec
mult1a	807.730 sec	4.730 sec
mult4	∞	17.900 sec
mult4(np)	∞	13.140 sec

Depth-first vs Memoization(first solution)

Program	Depth-First	Memoization
plus'4	0.08 sec	0.180 sec
plus4	0.1 sec	0.430 sec
plus4(np)	22.770 sec	1.95 sec
sub'1a	0.050 sec	0.240 sec
sub1b	0.250 sec	5.020 sec
sub3b	0.350 sec	8.160 sec
mult1(np)	1 133.490 sec	4.690 sec
mult1a	0.160 sec	2.900 sec
mult4	0.250 sec	7.150 sec
mult4(np)	∞	13.020 sec

Evaluation

- Simple memoization improves performance
- #Entries in table < 300
- #SuspendedGoals < 200
- Quick failure is important for program development
- Overhead of memoization may hurt performance
- Multi-stage strategy delays the reuse of answers
SCC(strongly connected components)

Type-checker with explicit memoization?

- Investigate special memoization techniques [Davies,Pfenning00]
- Implementation is non-trivial.
- Proofs are larger.
- Sending and checking proofs takes longer.
- Harder to reason about this implementation

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Parsing into higher-order abstract syntax

Tokens T :

'forall' | 'exist' | 'and' | 'or' | 'imp' | 'not' | '(' | ')' | 'true' | 'false'

Propositions A :

atom P | $\neg A$ | $A \& A$ | $A \vee A$ | $A \Rightarrow A$ | true | false |

forall $x.A$ | exists $x.A$ | (A)

Precedence $\neg > \& > \vee > \Rightarrow$

Associativity

$\&$, \vee : left associative

\Rightarrow : right associative

Implementation (idea by D.S.Warren)

```
% implication -- right associative
fimp: fi Ctx S S' (P1 => P2)
      <- fo Ctx S ('imp' ; S1) P1
      <- fi Ctx S1 S' P2.

ci:   fi Ctx S S' P
      <- fo Ctx S S' P.

% disjunction -- left associative
for:  fo Ctx S S' (P1 v P2)
      <- fo Ctx S ('or' ; S1) P1
      <- fa Ctx S1 S' P2.
```

Iterative Deepening vs Memoization

Length of input	Iter. deepening	Memoization
5	0.020 sec	0.010 sec
20	1.610 sec	0.260 sec
32	208.010 sec	2.020 sec
56	∞	7.980 sec
107	∞	86.320 sec

Evaluation

- Memoization outperforms iter. deepening
- Iterative deepening requires depth-bound
 - Failure meaningless
 - No decision procedure
- #Entries in table < 1000
- #SuspendedGoals < 1100
- Remarks
 1. Unambiguous parser
 2. Representing tokens as facts

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Higher-order theorem proving

- Tactics and tacticals
 - Isabelle [Paulson86], λ Prolog [Miller91, Felty93]
 - Need to be rewritten for each specification
 - Requires understanding of prover
 - Proving correctness of tactics often hard
- Memoization-based search
 - User concentrates on specification
 - Generic proof search mechanism
 - Table may contain useful failure information

Deterministic search: an alternative?

- Safe cut: finds exactly one solution
- In general: incomplete
- If there are only ground goals, then deterministic search is complete.
- Less general than memoization-based search
- No overhead

Conclusion

Memoization-based search allows

- generic efficient theorem proving
- execution of more declarative specification
- more efficient execution of implementations
- more flexibility
- small proofs

Memoization has some overhead

- Mixing tabled and non-tabled computation
- Table access
- Table size

Future work

- Higher-order indexing
- Different table strategies
- Incorporate into meta-theorem prover *Twelf* [Schürmann, Pfenning99]
- Applying tabelling to linear logic programming

Finally ...

Acknowledgements: Frank Pfenning

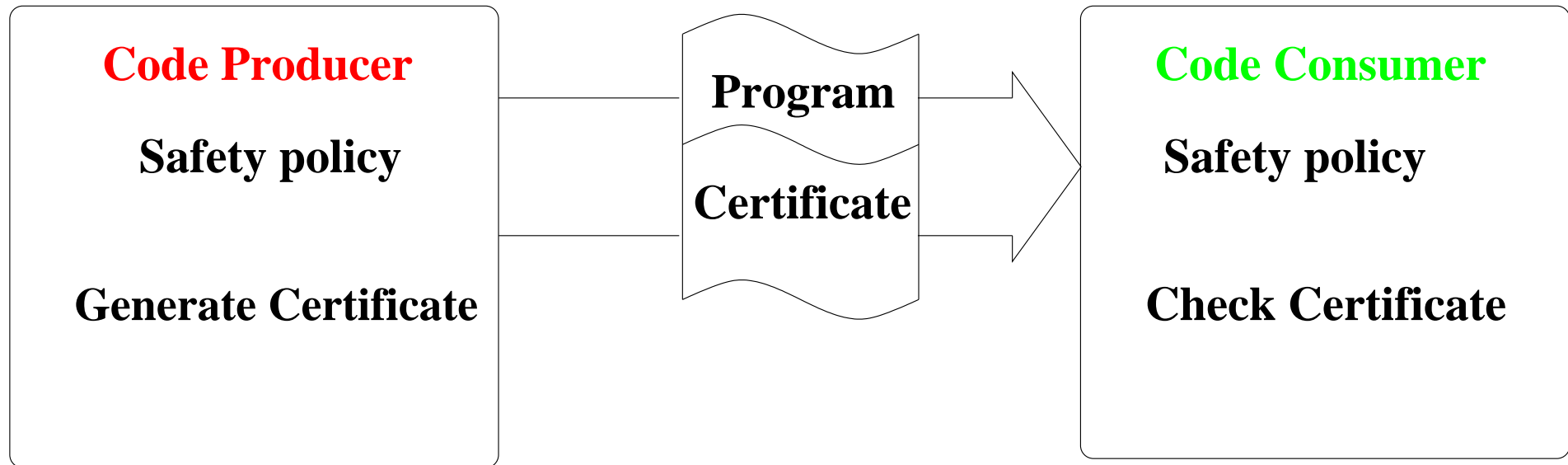
if you want to find out more:

Demo after workshop

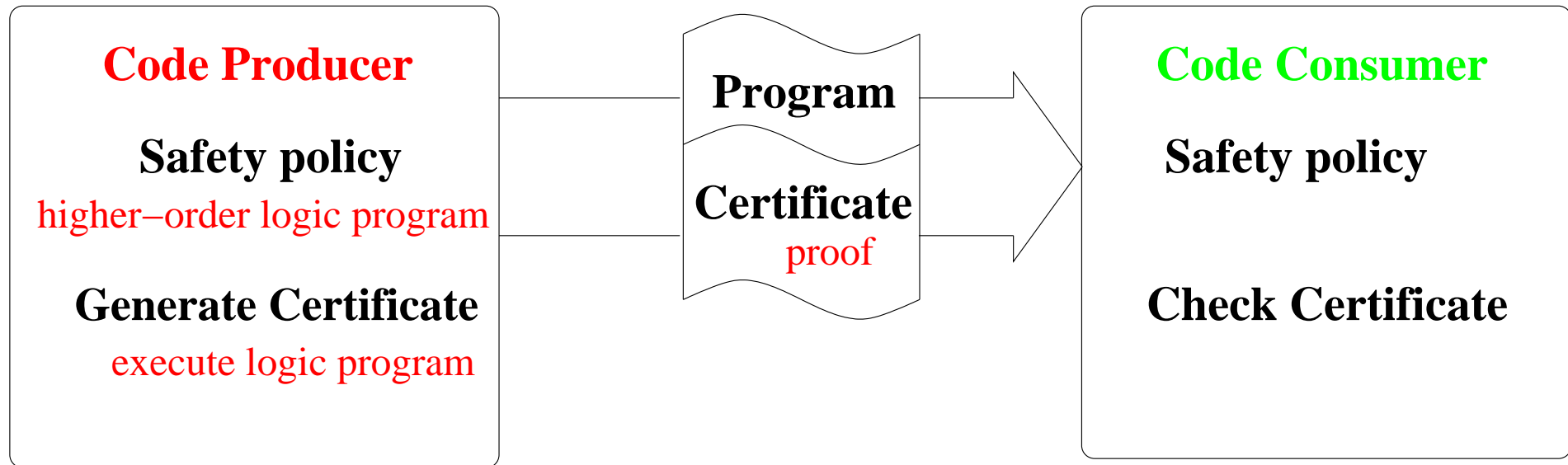
<http://www.cs.cmu.edu/~bp>

email: bp@cs.cmu.edu

Application: Certified code

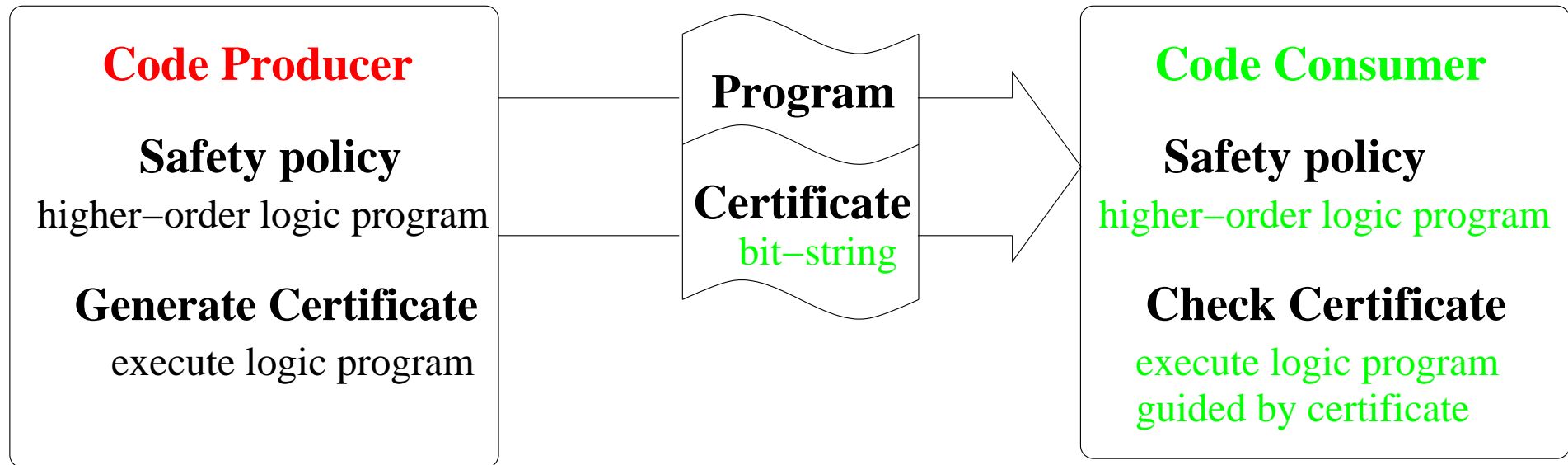


Application: Certified code



- Foundational proof-carrying code : [Appel, Felty 00]
- Proof-carrying authentication: [Felten, Appel 99]

Application: Certified code



- Foundational proof-carrying code : [Appel, Felty 00]
- Proof-carrying authentication: [Felten, Appel 99]
- Proof-checking via bit-strings: [Necula, Rahul 01]

Runtime, #Entries, #SuspGoals

Program	Run-Time	#Entries	#SuspGoals
plus'4	2.330 sec	151	48
plus4	3.150 sec	171	74
plus4(np)	1.95 sec	143	56
sub'1a	0.240 sec	58	11
sub1b	7.560 sec	252	138
sub3b	11.200 sec	278	170
mult1(np)	4.690 sec	217	83
mult1a	4.730 sec	211	78
mult4	17.900 sec	298	270
mult4(np)	13.140 sec	275	194

Time, #Entries, #SuspGoals

Length of input	Memoization	#Entries	#SuspGoals
5	0.010 sec	15	11
20	0.260 sec	60	54
32	2.020 sec	176	197
56	7.980 sec	371	439
107	86.320 sec	929	1185